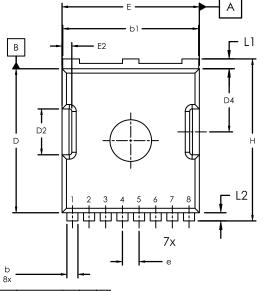
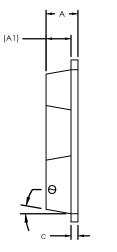


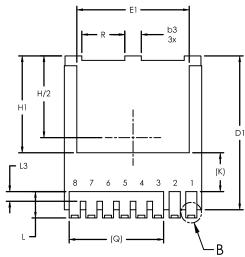
## H-PDSO-F8 9.90x10.38x2.30, 1.20P

CASE 740AA ISSUE B

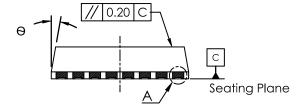
DATE 24 JUN 2025

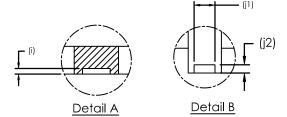






## **⊕** 0.25**M** C A B





## Note:

- 1. Dimensioning and tolerancing as per ASME Y14.5 2018
- 2. Controlling dimension: millimeters
- 3. Dimensions does not include Burrs and Mold Flashes

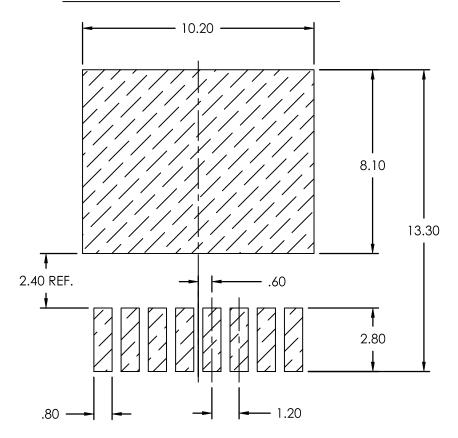
TO-LL						
CAMBOL	Value					
SYMBOL	Min	Nom	Max			
Α	2.15	2.30	2.45			
A1	1.80 REF					
b	0.65	0.80	0.90			
b1	9.65	9.80	9.95			
b3	1.10	1.20	1.30			
c D	0.40	0.50	0.60			
	10.18	10.38	10.58			
D1	10.88	11.08	11.28			
D2	3.15	3.30	3.45			
D4	4.40	4.55	4.70			
Е	9.70	9.90	10.10			
E1	7.95	8.10	8.25			
E2	0.60	0.70	0.80			
е	1.20 BSC					
H	11.48	11.68	11.88			
H1	6.80	6.95	7.10			
i	0.10 REF					
j1	0.46 REF					
j2	0.20 REF					
K	2.80 REF					
L	1.40	1.90	2.10			
L1	0.50	0.70	0.90			
L2	0.48	0.60	0.72			
L3 Q	0.30	0.70	0.80			
Q	6.80 REF					
R	3.00	3.10	3.20			
θ	10°					

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DESCRIPTION:	H-PDSO-F8 9.90x10.38x2.30, 1.20P		PAGE 1 OF 2

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**DATE 24 JUN 2025** 

## RECOMMENDED PCB LAND PATTERN



NOTE: LAND PATTERN AND THROUGH HOLE DIMENSIONS SERVE ONLY AS AN INITIAL GUIDE. END-USER PCB DESIGN RULES AND TOLERANCES SHOULD ALWAYS PREVAIL.

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