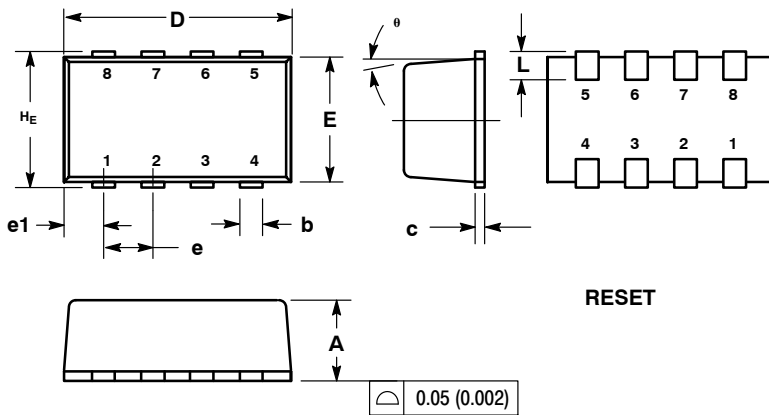


SCALE 1:1



**ChipFET™**  
CASE1206A-03  
ISSUE K

DATE 19 MAY 2009

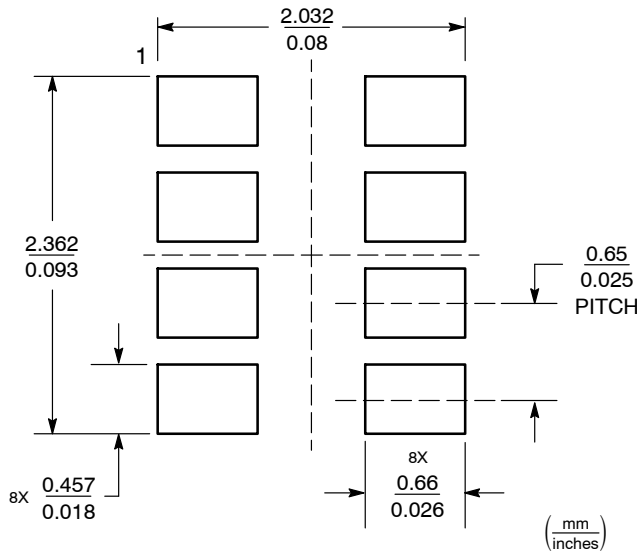
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. MOLD GATE BURRS SHALL NOT EXCEED 0.13 MM PER SIDE.
4. LEADFRAME TO MOLDED BODY OFFSET IN HORIZONTAL AND VERTICAL SHALL NOT EXCEED 0.08 MM.
5. DIMENSIONS A AND B EXCLUSIVE OF MOLD GATE BURRS.
6. NO MOLD FLASH ALLOWED ON THE TOP AND BOTTOM LEAD SURFACE.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.00	1.05	1.10	0.039	0.041	0.043
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	1.55	1.65	1.70	0.061	0.065	0.067
e	0.65 BSC			0.025 BSC		
e1	0.55 BSC			0.022 BSC		
L	0.28	0.35	0.42	0.011	0.014	0.017
HE	1.80	1.90	2.00	0.071	0.075	0.079
θ	5° NOM			5° NOM		

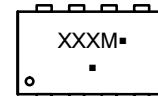
- |   |   |   |  |   |   |
|---|---|---|--|---|---|
| <p>STYLE 1:<br/>PIN 1. DRAIN<br/>2. DRAIN<br/>3. DRAIN<br/>4. GATE<br/>5. SOURCE<br/>6. DRAIN<br/>7. DRAIN<br/>8. DRAIN</p> | <p>STYLE 2:<br/>PIN 1. SOURCE 1<br/>2. GATE 1<br/>3. SOURCE 2<br/>4. GATE 2<br/>5. DRAIN 2<br/>6. DRAIN 2<br/>7. DRAIN 1<br/>8. DRAIN 1</p> | <p>STYLE 3:<br/>PIN 1. ANODE<br/>2. ANODE<br/>3. SOURCE<br/>4. GATE<br/>5. DRAIN<br/>6. DRAIN<br/>7. CATHODE<br/>8. CATHODE</p> | <p>STYLE 4:<br/>PIN 1. COLLECTOR<br/>2. COLLECTOR<br/>3. COLLECTOR<br/>4. BASE<br/>5. EMITTER<br/>6. COLLECTOR<br/>7. COLLECTOR<br/>8. COLLECTOR</p> | <p>STYLE 5:<br/>PIN 1. ANODE<br/>2. ANODE<br/>3. DRAIN<br/>4. DRAIN<br/>5. SOURCE<br/>6. GATE<br/>7. CATHODE<br/>8. CATHODE</p> | <p>STYLE 6:<br/>PIN 1. ANODE<br/>2. DRAIN<br/>3. DRAIN<br/>4. GATE<br/>5. SOURCE<br/>6. DRAIN<br/>7. DRAIN<br/>8. CATHODE / DRAIN</p> |
|---|---|---|--|---|---|

**SOLDERING FOOTPRINT**



Basic Style

**GENERIC MARKING DIAGRAM\***



- XXX = Specific Device Code
  - M = Month Code
  - = Pb-Free Package
- (Note: Microdot may be in either location)

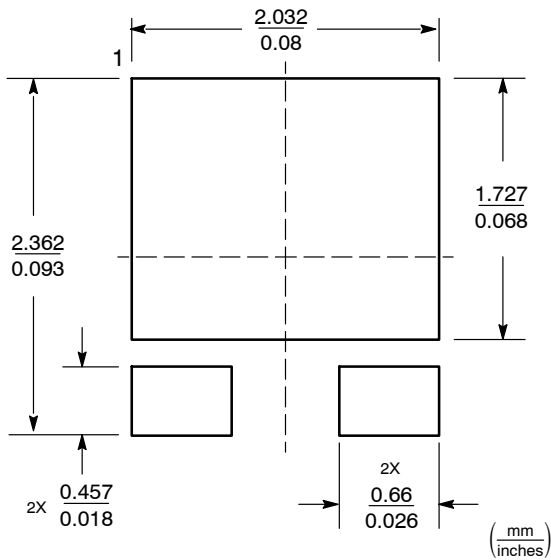
\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

**OPTIONAL SOLDERING FOOTPRINTS ON PAGE 2**

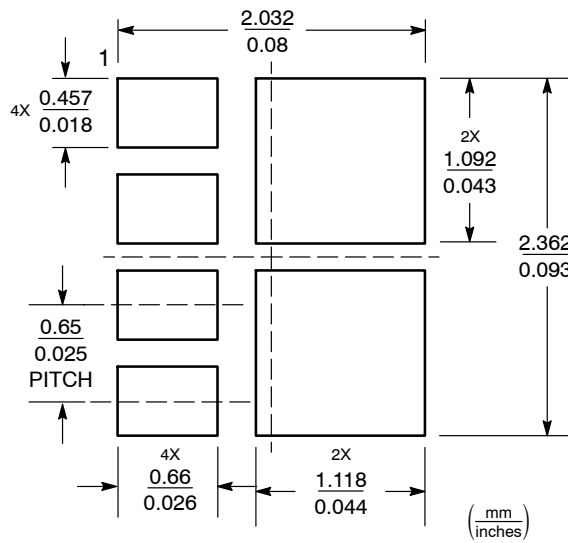
<b>DOCUMENT NUMBER:</b>	<b>98AON03078D</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>ChipFET</b>	<b>PAGE 1 OF 2</b>

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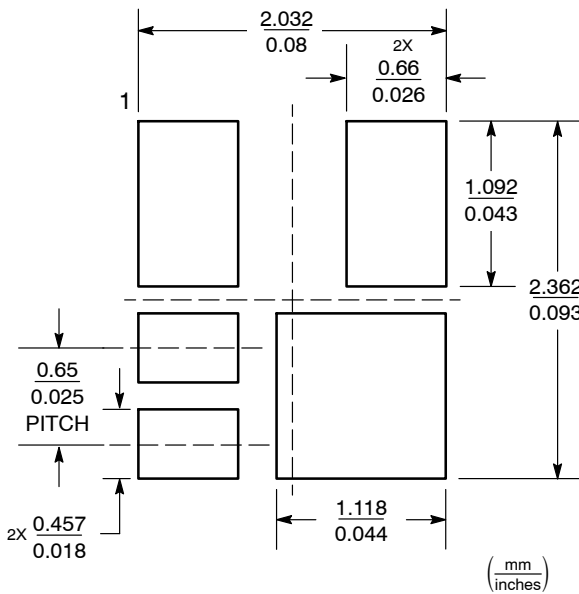
ADDITIONAL SOLDERING FOOTPRINTS\*



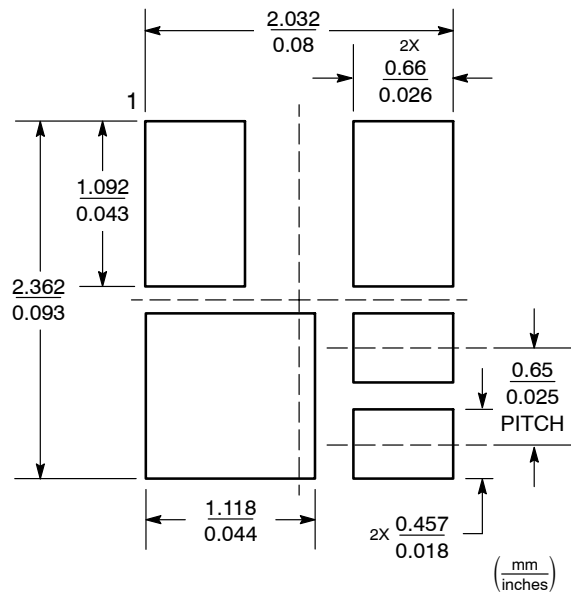
Styles 1 and 4



Style 2



Style 3



Style 5

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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