

SiC JFET Division

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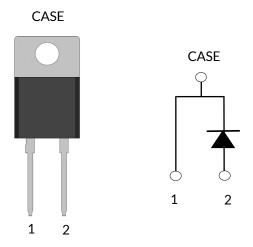








UJ3D1202TS



Part Number	Package	Marking
UJ3D1202TS	TO-220-2L	UJ3D1202TS











Silicon Carbide (SiC) Diode - EliteSiC, TO-220-2L, 2 A, 1200 V SiC Merged PiN-Schottky (MPS) Diode

Rev. D, Jan 2025

Description

UnitedSiC offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

Features

- Maximum operating temperature of 175°C
- Easy paralleling
- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- Excellent thermal performance, Ag sintered
- 100% UIS tested
- AEC-Q101 qualified
- Power converters
- Industrial motor drives
- Switch mode power supplies
- Power factor correction modules













Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units	
DC blocking voltage	V _R		1200	V	
Repetitive peak reverse voltage, T _J =25°C	V_{RRM}		1200	V	
Surge peak reverse voltage	V_{RSM}		1200	V	
Maximum DC forward current	I _F	T _C = 164°C	2	Α	
Non-repetitive forward surge current	1	$T_C = 25$ °C, $t_p = 10$ ms	30	Α	
sine halfwave	I _{FSM}	$T_C = 110^{\circ}C, t_p = 10 \text{ms}$	27		
Repetitive forward surge current	1	$T_C = 25$ °C, $t_p = 10$ ms	14.8	٨	
sine halfwave, D=0.1	I _{FRM}	$T_C = 110^{\circ}C, t_p = 10 \text{ms}$	8.8	Α	
Non-repetitive peak forward current		$T_C = 25^{\circ}C, t_p = 10 \mu s$	250		
	I _{F,max}	$T_C = 110^{\circ}C, t_p = 10\mu s$	250	Α	
•2.	∫i²dt	$T_C = 25$ °C, $t_p = 10$ ms	4.5	A^2s	
i ² t value		$T_C = 110^{\circ}C, t_p = 10 \text{ms}$	3.6		
Power dissipation	P _{tot}	T _C = 25°C	75	W	
		T _C = 164°C	5.5		
Maximum junction temperature	$T_{J,max}$		175	°C	
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at leads	T_{sold}	1.6mm from case for 10s	260	°C	

Thermal Characteristics

Doramotor	Symbol	Test Conditions	Value			Units
Parameter			Min	Тур	Max	Units
Thermal resistance, junction-to-case	$R_{\theta^{ m JC}}$			1.5	2	°C/W

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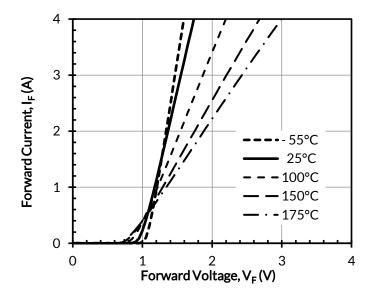


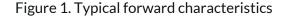
Electrical Characteristics (T_J = +25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Value			Units
			Min	Тур	Max	Offits
Forward voltage	V _F	I _F = 2A, T _J =25°C	-	1.4	1.6	V
		I _F = 2A, T _J =150°C	-	1.85	2.3	
		I _F = 2A, T _J =175°C	-	2	2.6	
Reverse current	I _R	V _R =1200V, T _J =25°C	-	2	22	μΑ
		V _R =1200V, T _J =175°C	-	60		
Total capacitive charge ⁽¹⁾	Q _C	V _R =800V		12		nC
Total capacitance	С	$V_R=1V, f=1MHz$		109		pF
		V _R =400V, f = 1MHz		11.5		
		V _R =800V, f = 1MHz		9.8		
Capacitance stored energy	E _C	V _R =800V		3.6		μЈ

(1) Q_c is independent on T_J , di_F/dt , and I_F as shown in the application note USCi_AN0011.

Typical Performance Diagrams





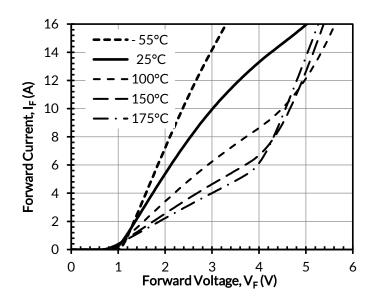


Figure 2. Typical forward characteristics in surge current

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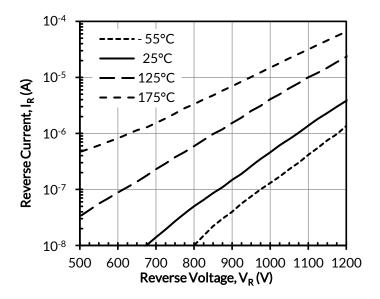












75

N 50

25

50

25

50

75

100

125

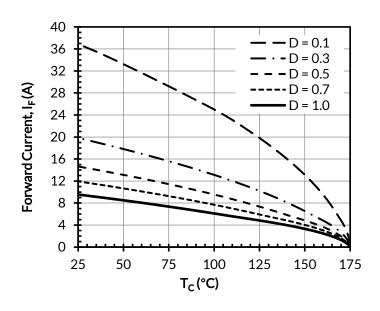
150

175

T_C (°C)

Figure 3. Typical reverse characteristics

Figure 4. Power dissipation



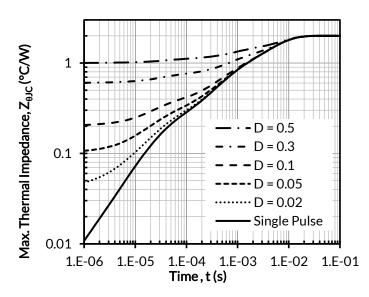


Figure 5. Diode forward current

Figure 6. Maximum transient thermal impedance



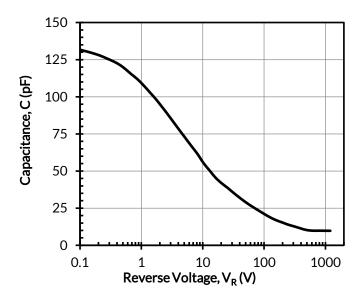












 $Q_{\rm c}$ (nC) $Q_{C} = \int_{0}^{V_{R}} C(V) dV$ Reverse Voltage, $V_R(V)$

Figure 7. Capacitance vs. reverse voltage at 1MHz

Figure 8. Typical capacitive charge vs. reverse voltage

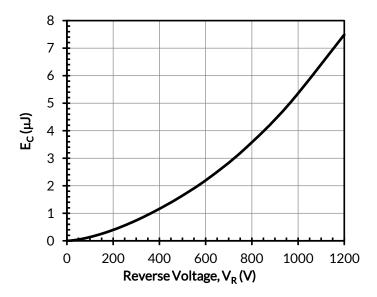


Figure 9. Typical capacitance stored energy vs. reverse voltage













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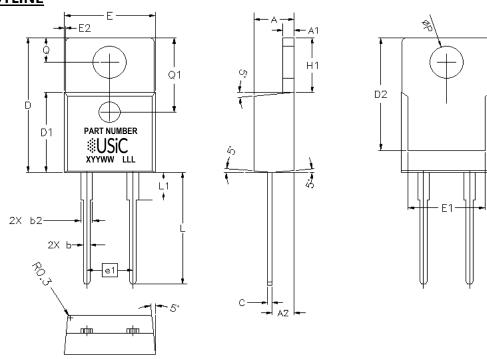
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TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PACKAGE OUTLINE

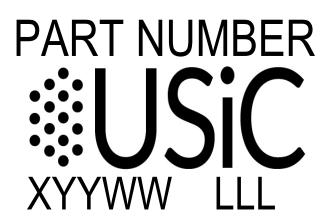


DIM	INC	HES	MILLIN	METERS
	MIN	MAX	MIN	MAX
Α	0.140	0.190	3.56	4.83
A1	0.020	0.055	0.51	1.40
A2	0.080	0.115	2.03	2.92
b	0.015	0.040	0.38	1.02
b2	0.040	0.070	1.02	1.78
С	0.014	0.030	0.36	0.76
D	0.560	0.650	14.22	16.51
D1	0.330	0.370	8.38	9.40
D2	0.480	0.517	12.19	13.13
E	0.380	0.420	9.65	10.67
e1	0.200 BSC		5.08	BSC
E1	0.270	0.350	6.86	8.89
E2	-	0.030		0.76
L	0.495	0.580	12.57	14.73
L1	-	0.250	1	6.35
ØΡ	0.139	0.161	3.53	4.09
Н	0.230	0.270	5.84	6.86
Q	0.100	0.135	2.54	3.43
Q1	0.330	0.340	8.38	8.64



TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PART MARKING



PART NUMBER = REFER TO
DS PN DECODER FOR DETAILS

X = ASSEMBLY SITE

YY = YEAR

WW = WORK WEEK

LLL = LOT ID

PACKING TYPE

ANTI-STATIC TUBE

QUANTITY /TUBE: 50 UNITS

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